Ph. D. ADMISSION ANNOUNCEMENT
DEPARTMENT OF ELECTRONICS SCIENCE
UNIVERSITY OF DELHI SOUTH CAMPUS (UDSC)
Admission to Ph.D. Programme 2015-16

The Department of Electronic Science, UDSC invites applications for Full Time Ph.D. programme for the academic year 2015-16

A total of **05 (Five) seats** are available in the areas of

- Modeling and Simulation of Micro-electronic Devices -Two
- Modeling and Simulation of Interband/ Plasmonic Solar Cells -One
- Modeling and Simulation of Nano-electronic Devices - One
- Design and Simulation of Photonic Devices -One

Eligibility: The candidate should have a minimum of 55% marks (or equivalent CGPA score) in Master's Degree/M.Phil./M.Tech./M.S. from a recognized university/institute. 5% relaxation will be given for OBC/SC/ST and Persons with Disability.

How to apply:

- All candidates interested in Ph.D admission should submit duly filled on-line application form available at http://admission.du.ac.in/phd/
- Candidates applying for more than one department are required to fill up separate forms for each department.

Selection procedure:

- Candidates will be selected on the basis of an Entrance Test comprising of a Written Test followed by an Interview.

- Written Test will be for a duration of 90 minutes based on Multiple Choice Questions

- Candidates holding (i) a valid national level research fellowships (ii) research positions in research projects in the department or (iii) awarded lectureship in the CSIR-UGC NET (or equivalent examination) are exempted from the written test, but will be required to appear for the interview for selection. For details, see Ordinance VI-B of the University of Delhi.
  http://www.du.ac.in/du/uploads/Rules_Policies_Ordinances/Acts/05082015_Ord.%20VI_B%2020_07_2015.pdf

- Candidates appearing for the written examination will be short-listed for interview. The list of short-listed candidates will be valid until the next written test for Ph.D. admissions.

- Admission of the candidates for Ph.D. programme is based on their suitability to specific specializations and also subject to fulfilling all other conditions laid down in the Ordinance VI-B of the University of Delhi.

- Candidates who do not have any fellowship may be provided Non-NET UGC fellowship based on its availability and only if the candidate is finally selected for the Ph.D. program.

- There is no fee for the entrance test.

- The written test and interview will be held at University of Delhi South Campus.

- No TA/DA will be paid for attending the written test and interview.

Syllabus for Ph.D. Entrance Examination:

The written examination will be based on Masters Level Knowledge in the following areas:

1. Basic Network Analysis and Synthesis
2. Analog and Digital Circuits
3. Semiconductor Physics, Devices and Characterisation
4. Optical Electronics and Lasers
5. Signals and Systems
6. Communication Electronics
7. Engineering Mathematics
8. Computers and Computational Techniques

Important dates:

Portal for applications, open from**:**   **Midnight, 22nd September 2015**

Last date for application (portal closes):   **Midnight, 5th October 2015**

Written Test: **12thOctober, 2015, Monday**

                                     10.30 AM to 12.00 Noon

Announcement of list of

short-listed candidates: **4.00 PM 13thOctober, 2015**

                       (University and Departmental (http://electronics.du.ac.in)website)

Date of Interview: **10.00 AM onwards 14thOctober, 2015**

Final list of the selected candidates will be displayed on university and departmental website

Note: *Department reserves the right not to fill up the vacant seats of Ph.D program in case of non-availability of suitable candidate.*

For any technical queries for filling up the on-line application form please write to admission@du.ac.in

The Head
Department of Electronic Science