

Department of Electronic Science Fifth Admission List

Schedule of Admission for M. Sc. (Electronics)

Fifth Admission List Announcement – Verification of Documents, Approval of Admission and Payment of online admission fee	August 16, 2017 – August 18, 2017
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Note: -

1. For document verification and approval of admission official working hours between 10:00 am to 5:00 pm shall be observed.
2. After approval of admission, the applicant has to log on the **PG admission portal** to make online admission fee payment.

Avinashi Kapoor
16.8.17

Professor Avinashi Kapoor
Head
Department of Electronic Science
University of Delhi South Campus
New Delhi-110021

Category : ST (Entrance Based)

Note : In case of the tie at the last seat(s) the information of the candidates are displayed at the end of this admission list.

S.No.	Roll No.	Form No.	Name	Alloted Department/College	Final Entrance Marks	Combined Rank	Qualifying Marks %/GP	Category Filled by Candidate
1	10510181	17ESCI1052860	LAW SHANGRING	Department of Electronics	37	184	63.75	ST

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University of Delhi

Fifth Admission List – M. Sc. Electronics

Category: CW (Entrance Based)

Note : In case of the tie the last seat(s) the information of the candidates are displayed at the end of the this admission list

S. No	Roll No.	Form No.	Name	Allotted Department/Coll ege	Final Entrance Marks	Combined Rank	Qualifying Marks %GP	Category Filled by Candidates
1	10510050	17ESC110725 58	SIMRAN JEET SINGH REKHI	Department of Electronics	19	264	50.69	CW

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DISCLAIMER: Qualifying Examination Marks, given in this List, are provided by the applicants. The University does not take any responsibility for the accuracy of these marks.