

Department of Electronic Science

Seventh Admission List

Schedule of Admission for M. Sc. (Electronics)

Seventh Admission List Announcement – Verification of Documents, Approval of Admission and Payment of online admission fee	August 25, 2017 – August 29, 2017
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Note: -

1. For document verification and approval of admission official working hours between 10:00 am to 5:00 pm shall be observed.
2. After approval of admission, the applicant has to log on the **PG admission portal** to make online admission fee payment.

Avinashi Kapoor
Professor Avinashi Kapoor
Head
24.8.17
Department of Electronic Science
University of Delhi South Campus
New Delhi-110021

Category : UNRESERVED (Direct Admission)

Note : In case of the tie at the last seat(s) the information of the candidates are displayed at the end of this admission list.

S.No.	Form No.	Name	Alloted Department/College	Qualifying Exam. (%)	Category Filled by Candidate
1	17ESCI1078947	MANISH PATWAL	Department of Electronics	82.30	Unreserved

Avinashi Kapoor

24.8.21
Professor Avinashi Kapoor
Head
Department of Electronic Science
University of Delhi South Campus
New Delhi-110021

Category : OBC (Direct Admission)

Note : In case of the tie at the last seat(s) the information of the candidates are displayed at the end of this admission list.

S.No.	Form No.	Name	Department/College	Qualifying Exam. (%)	Category Filled by Candidate
1	17ESCI1022916	ANURAG SINGH	Department of Electronics	73.16	OBC Non-Creamy layer

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24/8/17