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Advertisement for the post of JRF in CSIR sponsored project

Applications are invited for the post of JRF for CSIR project no. 22(0724)/17/EMR-II with following details:

Project Title: "MODELING AND SIMULATION OF THE GATE ELECTRODE ENGINEERED AND DIELECTRIC POCKET STEEP SUB.THRESHOLD SWING DEVICE FOR LOW POWER DIGITAL CIRCUIT DESTGN AND OPTI CAL APPLICATIONS"

1.	Name of the post	Junior Research Fellow(JRF)
2.	Number of posts	One
3.	Consolidated monthly Fellowship	Rs. 12,000(Twelve thousand rupees only) +H. R. A (http://csirhrdg.res.in/irfsrfra2.htm)
4.	Name of the Sponsoring Agency	CSIR, Govt. of India
5.	Duration of Fellowship	6 Months
6.	Essential Qualifications	M.Sc. in Electronics/Physics with electronics with NET qualification in relevant discipline as per CSIR rules OR M.Tech. in Electronics Nanoscience and technology/VLSI/Allied Subjects with GATE
7.	Last Date to apply	10 days from the date of Advertisement

• Eligible candidates should apply with their CV via email to <u>csirproject2017@gmail.com</u> on or before 10 days from the date of advertisement with the subject marked as "CSIR".

- Candidates will be shortlisted for the interview based on the merit and will be informed via email.
- Candidates shortlisted for interview should appear in person with originals of degrees, Certificates and date of birth proof along with any other relevant information (like copies of publications, awards etc).
- For technical information of the project and any other query to the post, the candidate may contact the principal investigator at <u>csirproject2017@gmail.com</u>.
- No travelling or any other allowances is admissible for attending the interview.

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अध्यक्ष / HEAD 1,5 - 19 इलैक्ट्रॉनिक विज्ञान विभाग Department of Electronic Science दिल्ली विश्वविद्यालय दक्षिण परिसर University of Delhi South Campus नई दिल्ली-१९००२१ / New Delhi-110021