

दिल्ली विश्वविद्यालय UNIVERSITY OF DELHI

Ref.: No. R&P/041/Advt. 305/2024/Asstt. Prof.(FOT)/077

Dated: 27.02.2025

NOTICE

- 1. This is in continuation to the notice ref. no. R&P/041/Advt. 305/2024/Asstt. Prof. (FOT)/073 dated:18.02.2025 regarding the screening status and written test for the post of Assistant Professor in the Department of Computer Engineering, (Post Code TDFT01), Electrical Engineering (Post Code TDFT02), and Electronics & Communication Engineering (Post Code TDFT03) in the Faculty of Technology, University of Delhi against the advt. no. R&P/2024/305 dated 27.09.2024.
- 2. In this connection, it is hereby informed to all the applicants that the schedule for the written exam has been modified. The applicants are advised to visit on their dashboard (generate their revised admit card as the timings of the Examination has changed) on the recruitment portal at the following address by using their respective login id and password:

www.du.ac.in – Work with DU – Advertisement Departments Advertisement for the post of Assistant Professor against the Advt. R&P/305/2024 dated 27.09.2024.

3. The details are as follows:

Sr. No.	Post Code	Name of the Post	Date of Examination	Timings of Examination
1.	TDFT01	Assistant Professor (Computer Engineering)	01.03.2025 (Saturday)	10:00 AM to 12:00 Noon
2.	TDFT03	Assistant Professor (Electronics & Communication Engineering	01.03.2025 (Saturday)	01:00 PM to 03:00 PM
3.	TDFT02	Assistant Professor (Electrical Engineering)	02.03.2025 (Sunday)	10:00 AM to 12:00 Noon

- 4. The centre of examination will remain same i.e. Arts Faculty, University of Delhi, South Delhi Campus, Benito Juarez Road, New Delhi-110021. No TA/DA shall be paid by the University of Delhi for the same.
- 5. The other things will remain unchanged as mentioned in the notice ref. no. R&P/041/Advt. 305/2024/Asstt. Prof. (FOT)/073 dated:18.02.2025.
- 6. This has been issued with the approval of the Competent Authority.

Assistant Registrar (Recruitment)