

**Department of Electronic Science
University of Delhi South Campus**

Advertisement for a JRF Position in the SERB-DST funded project

Fresh Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow for the project entitled "**Design, Optimization and Fabrication of Radiation Hardened Sub 0.15micron gate length InAlN/GaN HEMT for Ku band application.**" under SERB, Department of Science and Technology (DST), Government of India (File Number: SPG/2021/003067).

Project Title	Design, Optimization and Fabrication of Radiation Hardened Sub 0.15 micron gate length InAlN/GaN HEMT for Ku band application
Project Duration	Upto 30 th June 2023 (Likely to be extended)
Principal Investigator (PI)	Senior Professor Mridula Gupta Department of Electronic Science University of Delhi South Campus, New Delhi 110021
Co-Principal Investigator (PI)	Professor Manoj Saxena Department of Electronics, Deen Dayal Upadhyaya College, University of Delhi, New Delhi 110078
No. of position available	One (01)
Fellowship	Rs. 31,000/- (Per month) + HRA as per norms
Essential Qualifications	M.Sc. in Electronics/Physics (with specialization in Electronics) <u>and/or</u> M. Tech./MS(R)/M.E./equivalent in Electronics and Communication Engineering/Microwave/VLSI (with valid NET and/or GATE score) with consistently good academic record and a minimum of 60% marks or an equivalent CGPA in the Master's degree.
Application Deadline	10 th February 2023 (10.02.23)

Application procedure:

- Interested candidates must apply with their CV along with Scanned Self Attested copies of educational certificates and mark sheets, class XII onwards; Self Attested scanned copy of UGC-NET/CSIR-NET/GATE qualified certificate and Self Attested Scanned copies of Proof for research experience, special achievements and publications, if any. The soft copies of all the above documents (pdf format) must be emailed to the Principal Investigator on does.dst.serb2022@gmail.com with the subject "Application for Junior Research Fellow (JRF) position, DST-SERB Project" **on or before 10th February 2023.**
- Candidates who are already employed should produce No Objection Certificate from their employers.
- Only shortlisted candidates will be informed through email and called for interview in person originals of degrees, certificated, and date of birth proof along with any other relevant information (like copies of publications, awards etc).
- The position mentioned is temporary and for the period of duration of 06 months (likely to be extended). If performance of candidate is found unsatisfactory, the position can be terminated with 1-month notice.
- No TA/DA will be paid for attending the interview.
- For any queries regarding position, please feel free to email the PI.


Sr. Professor Mridula Gupta
Principal Investigator

Department of Electronic Science
University of Delhi South Campus, New Delhi 110021

**DST-SERB Sponsored Project on
"Design, Optimization and Fabrication of
Radiation Hardened Sub 0.15 micron gate
length InAlN/GaN HEMT for Ku band application."**